Analytical Study of Performances of Monolayer and Bilayer Graphene FETs Based on Physical Mechanisms

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Abstract

Following the recents works of K.S. Novoselov and A.K. Geim [1] in 2004, the semiconductor industry has been attracted to carbon-based technologies in order to maintain the trend on low-cost and still reducible transistor structures. From circuit point of view, monolayer and bilayer Graphene FETs (GFETs) are being studied for high performance applications. Although monolayer graphene presents an energy band structure where there is no bandgap, its high thermal and electrical conductivity makes it suitable for high-frequency applications. On the other hand, bilayer graphene presents a tunable bandgap by the application of an electric field perpendicular to the layer which allows the design of more flexible GFET structures. In this paper, we report the evidence of a tunable bandgap on bilayer graphene FETs. The presented results are based on GFET structures with an HfO₂ top gate dielectric ($\epsilon_r = 16$) and a SiO₂ back gate dielectric ($\epsilon_r = 3.9$) as in [4]. The gate length is fixed to 1 µm and the width to 2.1 µm. Residual carrier density n_{puddle} is considered to be 1.5e12 cm⁻² and the net doping concentration to -5e12 cm⁻². Access resistances R_S and R_D are set to be equal to 100 Ω . Electron and hole mobilities are set to 1500 cm²/V s.

For the simulations shown below, the effect of a tunable bandgap on a AB-stacking bilayer graphene (Figure 1) due to the applied electric field has been taken into account. Figure 2 shows the low energy bands for bilayer graphene for two different values of the applied potential energy. When the applied potential energy is zero (U = 0 eV) there is no bandgap ($E_{gap} = 0$), on the other hand when the applied potential energy is non-zero (i.e. U = 1 eV), the presence of a non-zero energy band gap can be observed. Figure 3 shows the Energy band gap Egap under the average electrical displacement field generated by the applied bias voltages [2]. A variation on Egap within [0 to 250 meV] can be noted. For the purpose of a comparative study, the monolayer and bilayer GFETs are compared via numerical simulations. Based on [3] and [4], a model for monolayer GFET has been developed where access resistances, the effect of puddles on carrier density and doping concentration are taken into account. In the case of bilayer GFETs a model based on physical equations [5] has been developed. Figure 4 shows a comparison of the IDS-VGS plot for bilayer and monolayer GFETs under the same bias conditions V_{DS} = [-1.5, -1.0, -0.5] V and a back-gate voltage V_{BG} = -40 V. Higher currents on bilayer GFETs can be observed from Figure 4 and a higher transconductance g_m reflecting a better current modulation in the channel by the gate voltage. Figure 5 shows the typical I_D-V_{DS} characteristic for both bilayer and monolayer GFETs under the same bias conditions Vgs = [0 to 3.0] V and V_{BG} = -40 V. Higher currents and higher output conductance gds are observed in the case of bilayer GFETs. The effect of a backgate voltage V_{BG} is shown in Figure 6 from which it can be observed that the current modulation stays constant for all V_{BG} as the ambipolar point (V_{Dirac}) moves along de x-axis. Extraction of small-signal parameters from both the GFETs allows us to calculate the cut-off frequency f_T as a function of gate length (Figure 7). Higher cut-off frequencies, f_T, can be observed for bilayer graphene for all gate lengths. Scaling down gate lengths for both bilayer and monolayer GFETs show an increase in f_T. Thus, from our study, we can infer some important conclusions about the possible future of GFETs as bilayer graphene offers higher cut-off frequencies and a tunable band gap over monolayer devices. Even if voltage gain ($A_V = g_m/g_{ds}$) is higher in bilayer graphene FETs, it is still to be improved for future high-performance applications.

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References

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Figure 1: AB - stacking bilayer graphene



Figure 3: Bandgap variation under average electrical displacement fields



Figure 4: $I_{DS}(V_{GS})$ for monolayer (red) and bilayer (blue) GFETs for V_{DS} = [-1.5, -1.0, -0.5] V, V_{BG} = -40 V and R_S = R_D = 100 Ω .



Figure 6: $I_{DS}(V_{GS})$ for monolayer (red) and bilayer (blue) GFETs for V_{DS} = -1.0 V, V_{BG} = [0 to -60] V and R_S = R_D = 100 Ω .



Figure 2: Low energy bands for bilayer graphene for two values of the applied potential energy



Figure 5: $I_{DS}(V_{DS})$ for monolayer (red) and bilayer (blue) GFETs for V_{GS} = [-3 to 0], V_{BG} = -40 V and R_S = R_D = 100 Ω .



Figure 7: f_T as a function of gate length (L) for monolayer (red) and bilayer (blue) GFETs for V_{DS} = 1.0 V, V_{BG} = -40 V, V_{GS} = 0.5 V and R_S = R_D = 100 Ω .